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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 100571E5	FILING DATE 01/25/2002	CLASS 361	SUBCLASS 12	GAU 2834	EXAMINER Bancroft
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**APPLICANTS: Adler Eric;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/616,951 07/14/2000 PAT 6,344,964

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** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	BUR919990222US2
Verified and Acknowledged Examiners's initials		
TITLE : Capacitor having sidewall spacer protecting the dielectric layer and method of fabricating the same		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-34)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
ISSUE FEE		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	
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